

**Notice of References Cited**

Application/Control No.

09/713,423

Applicant(s)/Patent Under  
Reexamination  
AGARWALA ET AL.

Examiner

Frank Duong

Art Unit

2666

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**NON-PATENT DOCUMENTS**

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	V	Comisky et al, A Scalable High-Performance DMA Architecture for DSP Applications, IEEE, pages 414-419, September 2000.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.